

ABSTRACT OF THE DISCLOSURE

A variety of special effect processing is performed on video frames retrieved from a disk device 30 in effect processing stages ES1, ES2, and so on. A work memory 112B is always interposed between processing at one stage and processing at the next stage. Further, a plurality of processing are performed in parallel at one effect processing stage. Frames having all of effect processing completed are stored in an input/output buffer 115 in no special order through layering processing ESL. The plurality of edited frames stored in the input/output buffer 115 are output in synchronization with an output clock (not shown) in realtime.

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